EAST - [10812950.wsp:1]

File View Edit Tools Window Help

- Drafts
- Pending
- Active
  - L2: (7514) ((710/5) or (710/15) or (710/33) or (710/39) or (712/237) or (711/137) or
  - L3: (456) memory near9 hint
  - L4: (88) 2 and 3
- Failed
- Saved
  - S1: (709156) memory near9 (request access read write)
  - S8: (75) S7 not S6
  - S7: (82) (command packet request) with S5
  - S3: (34574) (subequent next future following) near9 S1
  - S6: (12) S3 with S5
  - S4: (29369) S3 near1 (request access read write)
  - S5: (446) memory near9 hint
- Favorites
- Tagged (7)
- UDC
- Queue
- Trash

Search   List   Browse   Queue   Clear

DBs   US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB   ☑ Plurals

Default operator: OR   ☐ Highlight all hit terms initially

BRS form   ☑ &R form   Image   Text   HTML

	U	🗹	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	②	③			Im
1	☐	☑	US 20050071542	20050331	15	Prefetch mechanism for use in a	711/105	711/137		Weber, Frederick D. et al.	☑	☐	☐	☐	☐	☐	☐	US
2	☐	☑	US 6633959 B2	20031014	30	Non-uniform memory access (N	711/141	711/119;		Arimilli; Ravi Kumar et al.	☑	☐	☐	☐	☐	☐	☐	US
3	☐	☑	US 6772295 B2	20040803	17	System and method for managi	711/137			Spencer; Thomas V. et al.	☑	☐	☐	☐	☐	☐	☐	US
4	☐	☑	US 6128706 A	20001003	12	Apparatus and method for a lo	711/141	711/144;		Bryg; William R. et al.	☑	☐	☐	☐	☐	☐	☐	US
5	☐	☑	US 6681302 B2	20040120	15	Page open hint in transactions	711/154			Cho; James Y. et al.	☑	☐	☐	☐	☐	☐	☐	US
6	☐	☑	US 6718440 B2	20040406	18	Memory access latency hiding	711/137	711/117;		Maiyuran; Subramaniam	☑	☐	☐	☐	☐	☐	☐	US
7	☐	☑	US 20030229762	20031211	14	Prefetcher for memory controlle				MAIYURAN, S et al.	☑	☐	☐	☐	☐	☐	☐	US

Hits   Details   HTML

Ready   CAP NUM